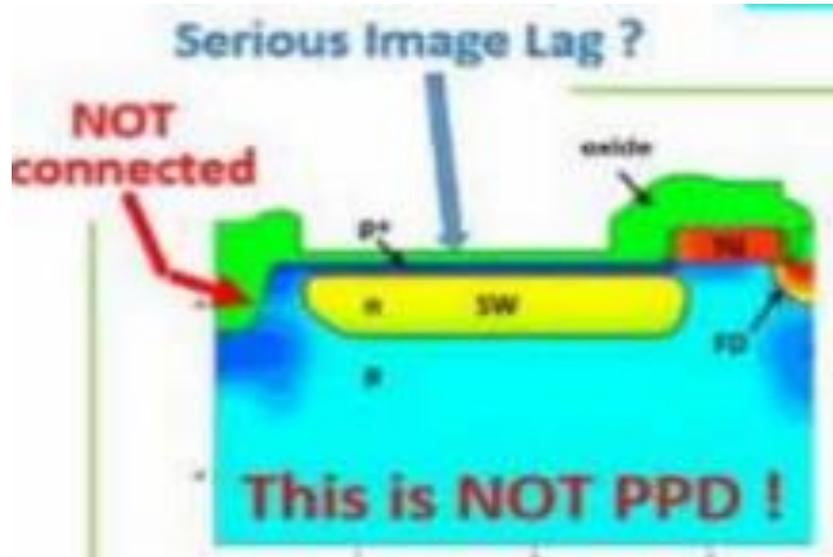
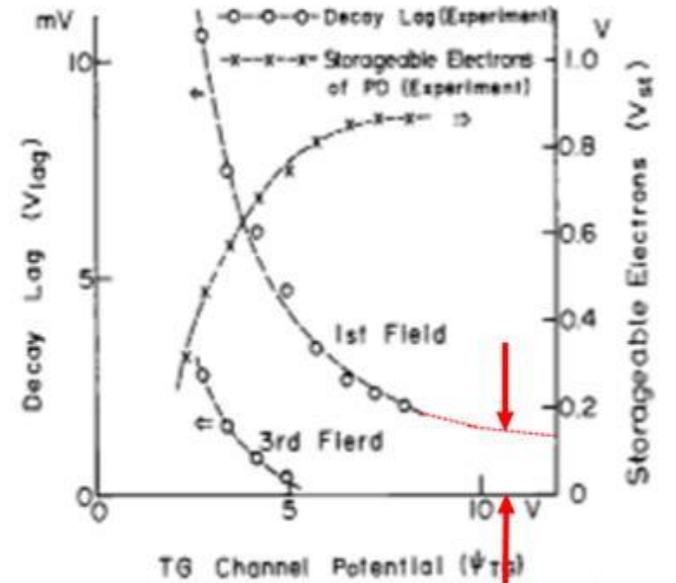
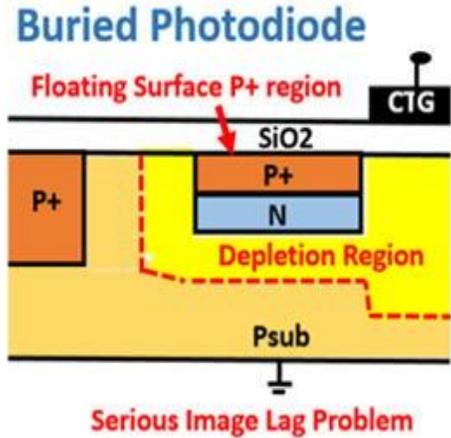


(1) Fossum PPD 2014



Difference of Buried Photodiode and Pinned Photodiode

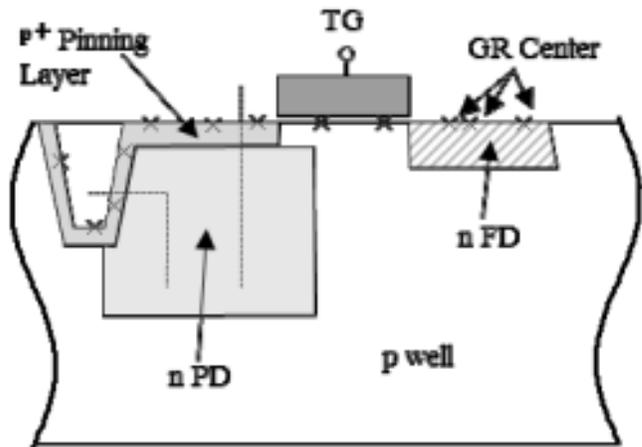
Figure 5 does not have the P+ channel stop nearby.



There is still image lag at the CTG gate voltage more than 10 volt.

Fig.6. Storageable electrons vs. transfer gate channel potential, and decay lag vs. transfer gate channel potential in the P⁺NP⁻ structure photodiode

(2) Teranishi PPD 2014



NEC IEDM1982 Paper

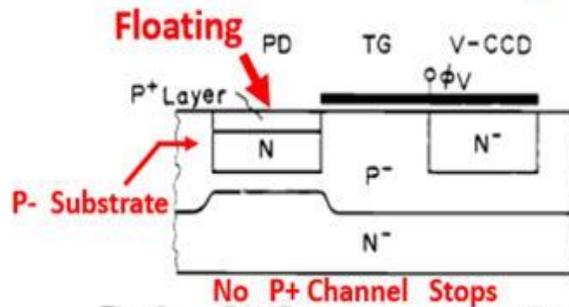


Fig.5. P⁺NP⁻ structure photodiode (a) Unit cell cross sectional view

NEC IEDM1982 Paper reported Image Lag

Figure 6 shows that there is still image lag at the CTG gate voltage of > 10 volt.