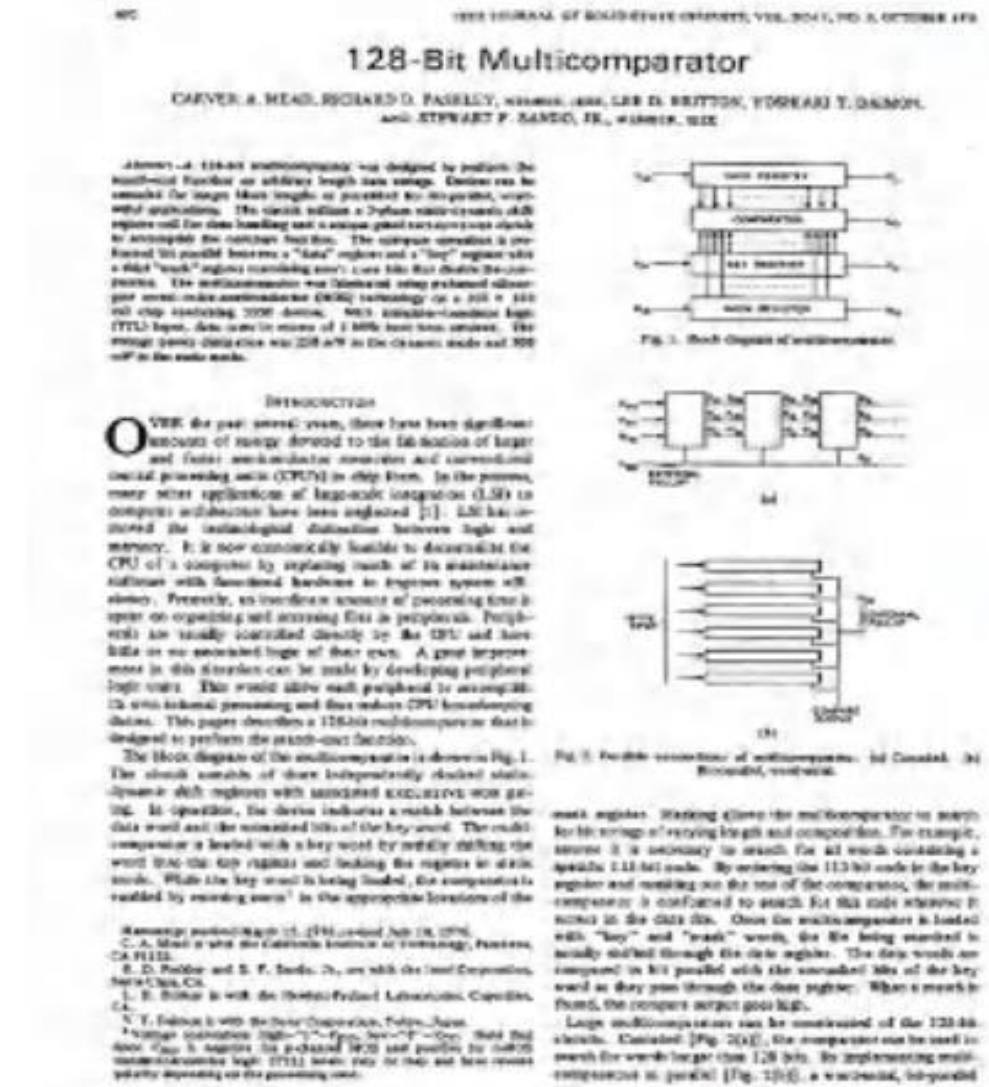
# Sweet Memory ♡

## 128-Bit Multicomparator Chip designed by Caltech Students and fabricated by Intel.

**Ref: IEEE Journal of Solid State Circuits, VOL.SC11, No.4, October 1976**



128-Bit Multicomparator

CARVER A. MEAD, RICHARD D. PASLEY, LEE D. BRITTON, YOSHIAKI T. DAIMON, and STEWART P. SANDO, JR.

Prof. C. A. Mead and Yoshiaki Daimon Hagiwara working on the silicon chip design at Caltech in 1972

128-bit Multicomparator chip, designed by Hagiwara in 1972-1973 and fabricated by Intel PMOS process.