



09-10 December, 2021
Cape Town - South Africa



ICECET

International Conference on Electrical,
Computer and Energy Technologies

09 - 10 December, 2021
Cape Town, South Africa

IMPORTANT NOTE: All times are in Cape Town time zone. (GMT+2).



CONFERENCE PROGRAM

Session E-III

December 9th, 2021

15:15-17:15

Parallel Session E - III: VLSI, MEMS, Microelectronics

Chair: Yoshiaki Hagiwara

ID	Authors	Title
61	Yoshiaki Hagiwara	Pinned Buried PIN Photodiode type Solar Cell
75	Yoshiaki Hagiwara	Invention and Historical Development Efforts of Pinned Buried Photodiode
295	Takashi Hosono, takahumi kamio, Souma Yamamoto, Jun-ichi Matsuda, Kouji Hirai, Shogo Katayama, Tianrui Feng, Anna Kuwana, Haruo Kobayashi, Akira Suzuki, Satoshi Yamada, Tomoyuki Kato, Ritsuko Kitakoga, Takeshi Shimamura, Gopal Adhikari, Nobuto Ono, Kazuhiro Miura	CMOS Nagata Current Sources with Self-Bias Configuration Insensitive to Supply Voltage and Temperature
471	Reza Hashemian	Design of Cost-Effective and Modular Analog Hearing Aides Using Nullors for Response Matching
474	John Suarez, Michael Shur	Terahertz Diagnostics SiGe Heterobipolar Junction Transistors
606	Edoardo Barteselli, Luca Sant, Richard Gaggi, Andrea Baschiroto	High Audio Band PSR and Fast Settling-Time Dual-Loop LDO Regulator Architecture for Low Power Application
619	Moulaheene Fateh	Chopper-stabilized Fully Differential Amplifier for Portable ECG Monitoring Systems using 90 nm Technology
694	Jasmine Bajaj, Babita Jajodia	Efficient Hardware Implementation of High-Speed Recursive Vedic Squaring Architecture on FPGA
171	Pietro Burrascano	Relevance of accurate filter design in Hammerstein model identification algorithms of nonlinear systems