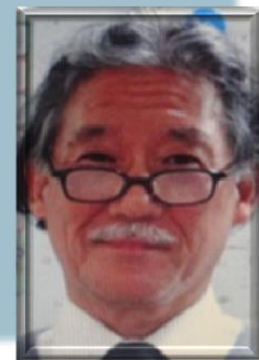


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BIOGRAPHY

Hagiwara graduated Caltech with BS71 with honor, MS1972 and PhD1975 in Physics Minor and in Electric Engineering Major. He joined Sony in 1975 and was engaged first in the early development of image sensors. Meanwhile, he was serving as a visiting professor in the electrical department and the applied physics at Caltech from 1998 to 1999. He was a visiting professor in the electrical department at Gunma University at Kiryu Japan from 2003 till 2006. While working for Sony from 1975 to 2008, he was engaged in the early developments of image sensor and the digital camera chip set including the ADC, DRAM and high-speed Cache SRAM buffer memory chips and micro controller chips. Before retiring from Sony he was engaged in the PS2 and PS3 chip set developments. He was invited to talk at CCD'79, ECS1980, ESSCIRC200, ESSICRC2008 and ISSCC2013 for his works at Sony from 1975 till 2008. Since 2009 till 2017, he taught graduate and undergraduate students as a full professor of the information science department at Sojo University, Kumamoto-city, Japan. And currently he is serving as a specially appointed professor in the president office of Sojo University.

In 1992 he also served as a member of JEDEC memory standardization committee and also as the IEC TC47 technical committee chair of the international standard committee (IEC). He also served as the international program chair and an operational committee member in IEEE EDS sponsored ICMTS conferences, IEEE ISSCC conferences for which he served as the ISSCC Asian Committee chair and also as the ISSCC international technical program (ITC) chair in series. He was also a member of the PC and OC and now advisory committee members of IEEE Cool Chips conferences in series. In 2008 he founded and worked as the president in the artificial intelligent partner system laboratory (AIPLAB consortium), a nonprofit research organization (NPO) registered by Kanagawa prefecture government in Japan in 2008.

He was the chair and is still serving as a member of the Department of Education in Society of Semiconductor Industry Specialists (SSIS) in Japan.

He is Caltech Distinguished Alumni, AAIA Fellow and IEEE Life Fellow.