
Albert supports that
Yoshi had the Pinned Photodiode idea
before Teranish 1980 Patent.

FROM: hagiwara [mailto:hagiwara-yoshiaki@aiplab.com]

SENT: Tuesday, July 10, 2018 3:19 PM

TO: 'a.theuwissen'

SUBJECT: RE: NEC-SONY Patent War

> Hi, Albert,
>
> how are you ?
> I am very mad at Fossum's 2014 paper
> which I found just a few weeks ago.
> I am now 70 years and completely retired..
> Fossum did not quote the very important
> Pain's work at JPL CalTech
> on the Global Shutter CMOS image sensor ...
>
> Yoshi

On Tue, 10 Jul 2018 18:24:23 +0900, hagiwara wrote:

> Albert,
>
> I am sorry my previous e-mail title was in Japanese,
> copying to many unrelated people.
> But they are also my friends.
> I am re-sending with more information
> on the Pain's paper.
> You know, I was also a visiting professor
> at CalTech during 1998 to 1999
> and frequently visiting Pain's Lab at JPL,
> CalTech during the period.
> I remember Pain and his team really hated
> Fossum at that time.
> I recall they called Fossum a thief.
> Kind regards
>
> Yoshi