

2024 International Symposium on Integrated Circuit Design and Integrated Systems

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Yoshiaki Daimon Hagiwara
Professor, Sojo University,
Kumamoto city, Japan

“Dynamic Photo Transistors for Image Sensors and Solar Cells”

Key Note Speaker

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**Professor, Sojo University,
Kumamoto city, Japan**

Research field : ADC, DRAM and High-speed Cache SRAM Buffer Memory Chips, Micro Controller Chips, Semiconductor Chips for AIPS Robotics, Solar Cell Energy Solutions.

Hagiwara graduated California Institute of technology (Caltech) in Pasadena, California in USA with BS71 with honor, MS1972 and PhD1975 with the major in Electric Engineering and with the minor in Physics. In February1975, he joined Sony in Tokyo, Japan and was engaged first in the early development of Sony image sensors. While working for Sony from 1975 to 2008, he was engaged in the early developments of image sensor and the digital camera chip set including the ADC, DRAM and high-speed Cache SRAM buffer memory chips and micro controller chips. Before retiring from Sony at the end of July 2008, he was engaged in the PS2 and PS3 chip set developments. He was invited to talk at CCD1979, ECS1980, ESSCIRC2001, ESSCIRC2008 and ISSCC2013. While working at Sony until July 2008, he was serving as a visiting professor at Caltech since 1998 to 1999 in the office of Prof. C. A. Mead in Electrical Engineering department and also in the offices of Prof. T.C. McGill and Prof. James McCaldin in Applied Physics department. He was also serving as a visiting professor since 2003 till 2006 in the office of Prof. Haruo Kobayashi in Electrical Engineering department at Gunma University in Japan.

In 1992 he also served as a member of JEDEC memory standardization committee and also as the IEC TC47 technical committee chair of the international standard committee (IEC). He also served as the international program chair and an operational committee member in IEEE EDS sponsored ICMTS conferences, IEEE ISSCC conferences for which he served as the ISSCC Asian Committee chair and also as the ISSCC international technical program committee (ITC) chair in series. He was also a member in the program committee (PC) and the operational committee (OC) and is now serving in the advisory committee (AC) of the IEEE Cool Chips conferences in series. He also taught from 2020 to 2021 the AIPS, Artificial Intelligent Partner System for AI robotics at Kanagawa Institute of Technology in Japan.



In 2008 he founded and worked as the president of Artificial Intelligent Partner System, AIPS, a nonprofit research organization, NPO, registered by Kanagawa prefecture government in Japan. Since 2017, he is serving as an operational committee (OC) member and also as a technical program committee (PC) member of Department of Education in Society of Semiconductor Industry Specialists (SSIS) in Japan. Since 2009 till 2017, he taught graduate and undergraduate students as a full professor of Information and Communication Science department and is still now serving as a specially appointed professor at the president office in Sojo University in Kumamoto-city, Japan working for developments of semiconductor chips for AIPS robotics and Solar Cell energy solutions. He is Caltech Distinguished Alumni, IEEE Life Fellow and AAIA Fellow.