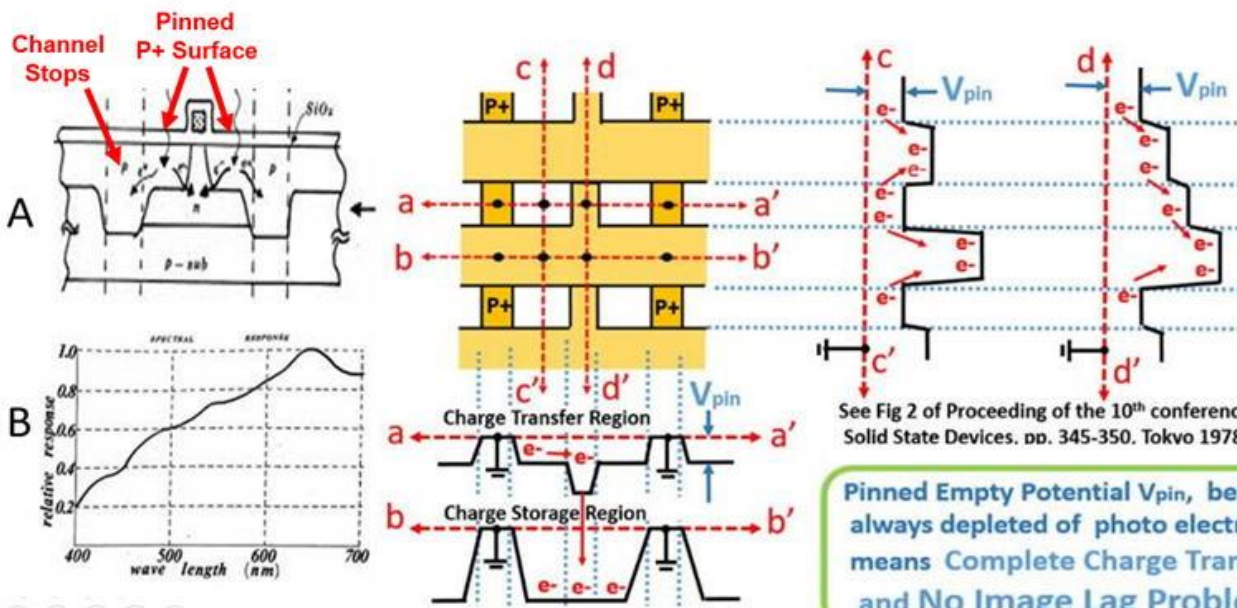


Super Light Sensitivity Feature

<http://www.aiplab.com/>
Yoshiaki Hagiwara



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Slide 14

This slide shows that the buried depletion pinned photodiode, used in the 1978 Frame Transfer CCD image sensor reported by Hagiwara in 1978 in his SSDM1978 paper.

The surface P+ potential is pinned to the ground potential and also the empty potential well under the pinned surface P+ layer is also pinned with no image lag problem.