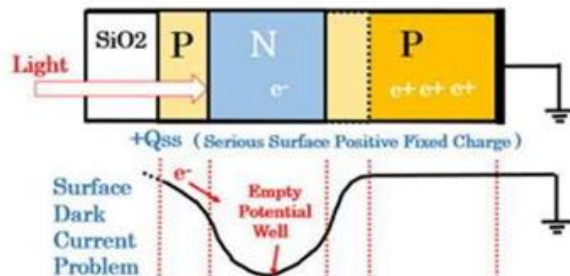


# Super Light Sensitivity Feature

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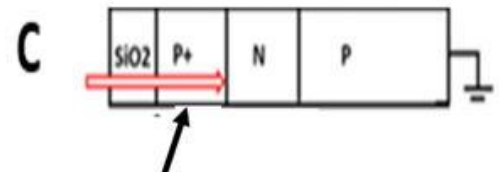
## P+NP junction type Buried Depletion Photodiode with Image Lag Problem

See Japanese Patent 1975-134985, 1975-127647 and 1975-197646



Depletion Photodiode by definition has the Complete Charge Transfer Mode similar to the Buried Channel CCD charge Transfer with Completely Depleted Empty Potential Well with No Image Lag

## Simple Buried Photodiode



The surface P+ layer may be Floating, not Pinned Photodiode

09

Slide 09

The Buried Depletion Photodiode shown in this figure, also with the floating surface P+ layer with the surface electric field, causes the same image lag problems with the similar reason why the classical N+P junction type photodiode inherently had the image lag problem.